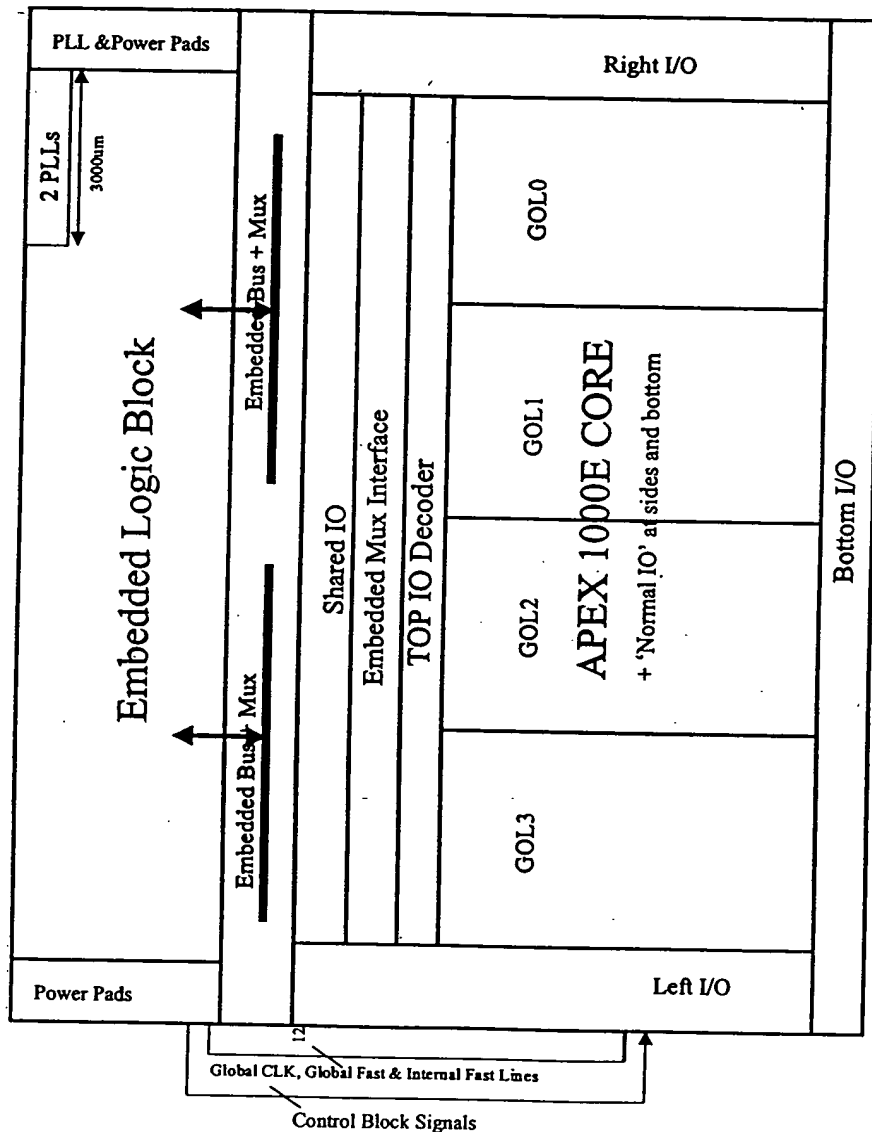


121

151

154



Top Level Floorplan

FIGURE 2

09881226-061201

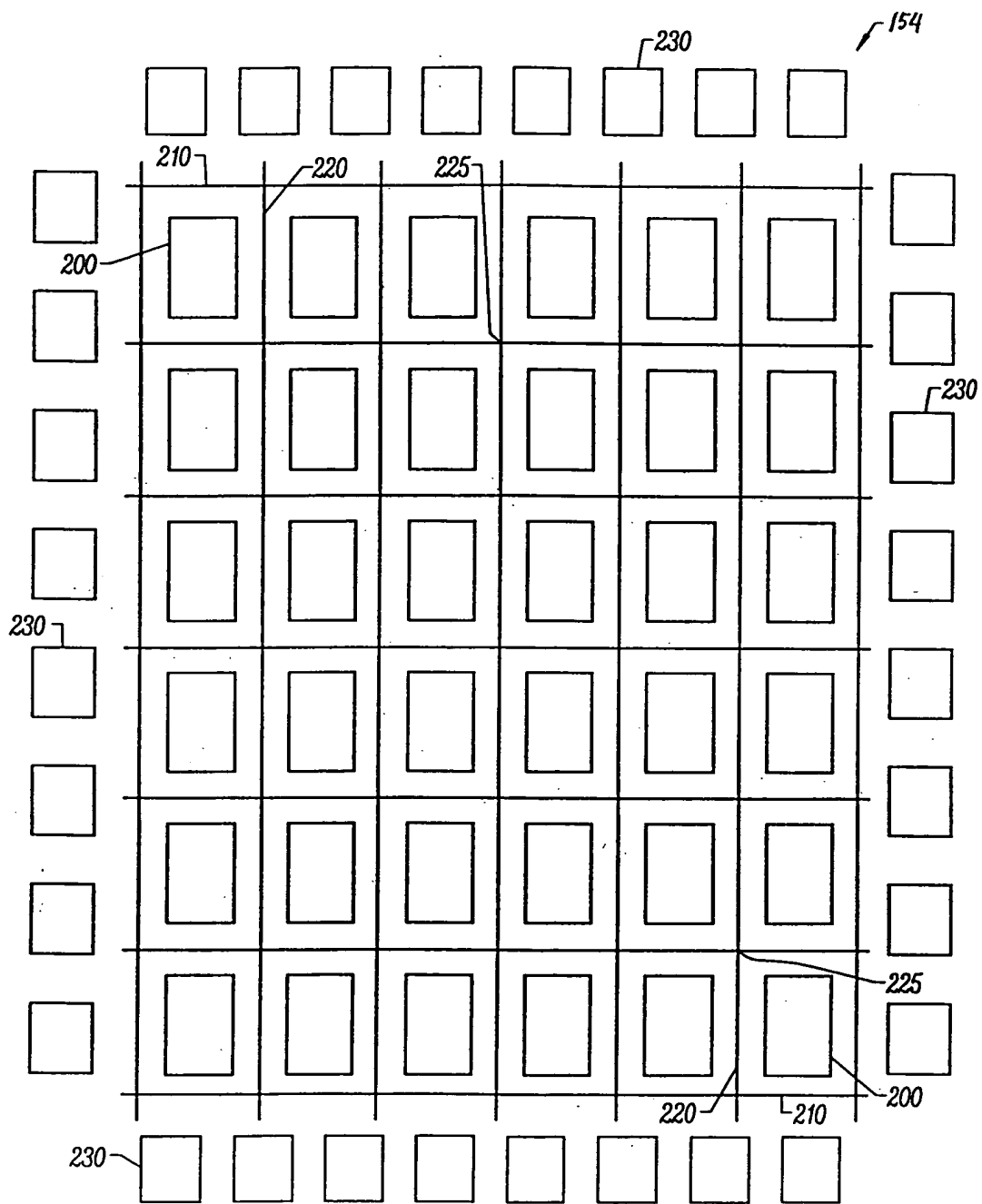


FIGURE 3

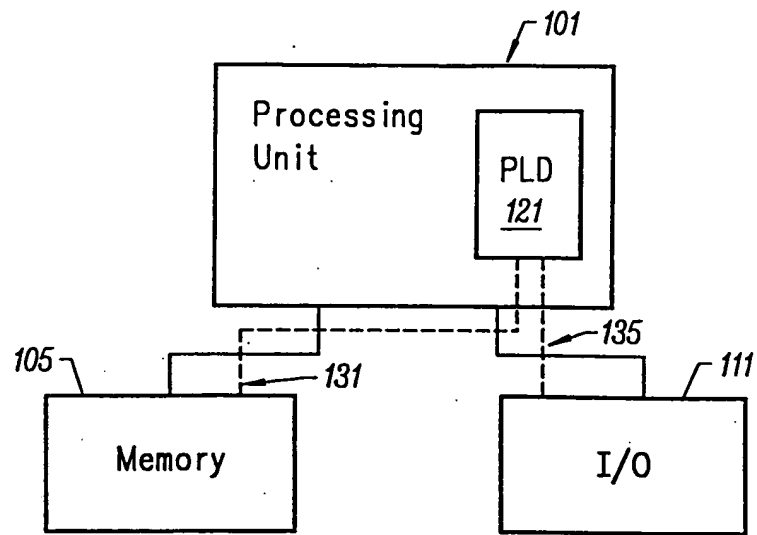


FIGURE 1

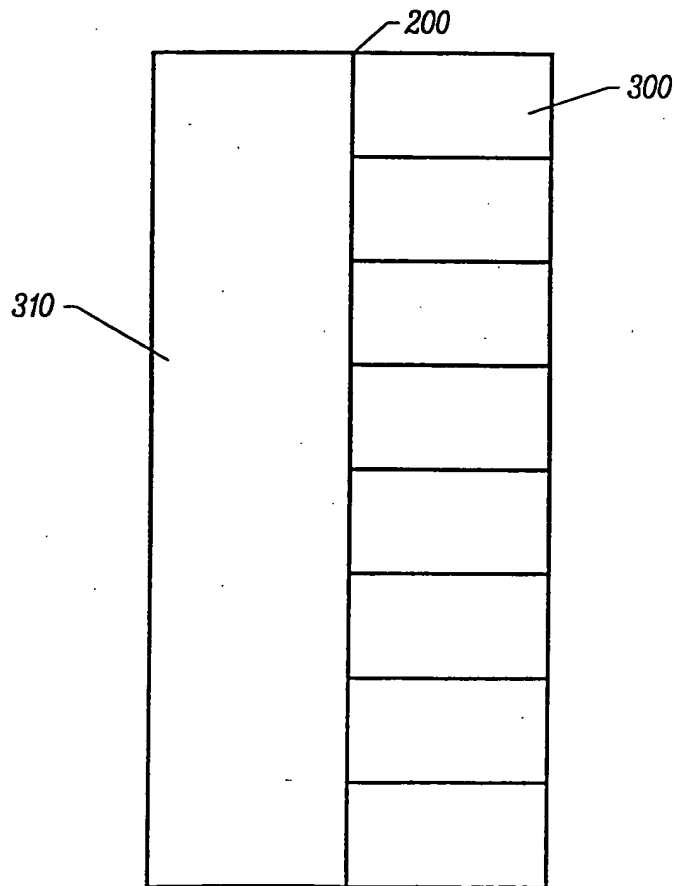


FIGURE 4

09881226-051201

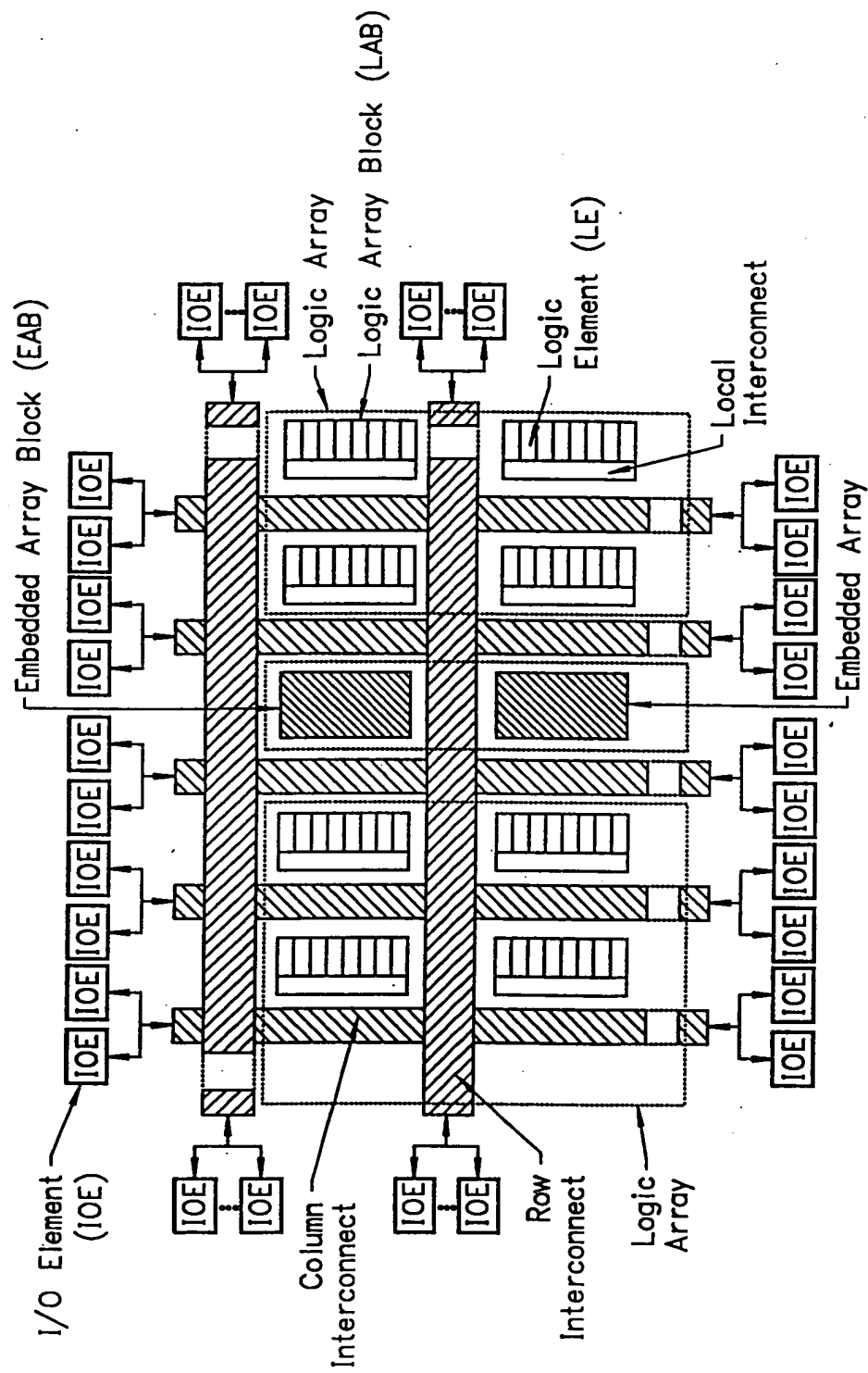


FIGURE 5

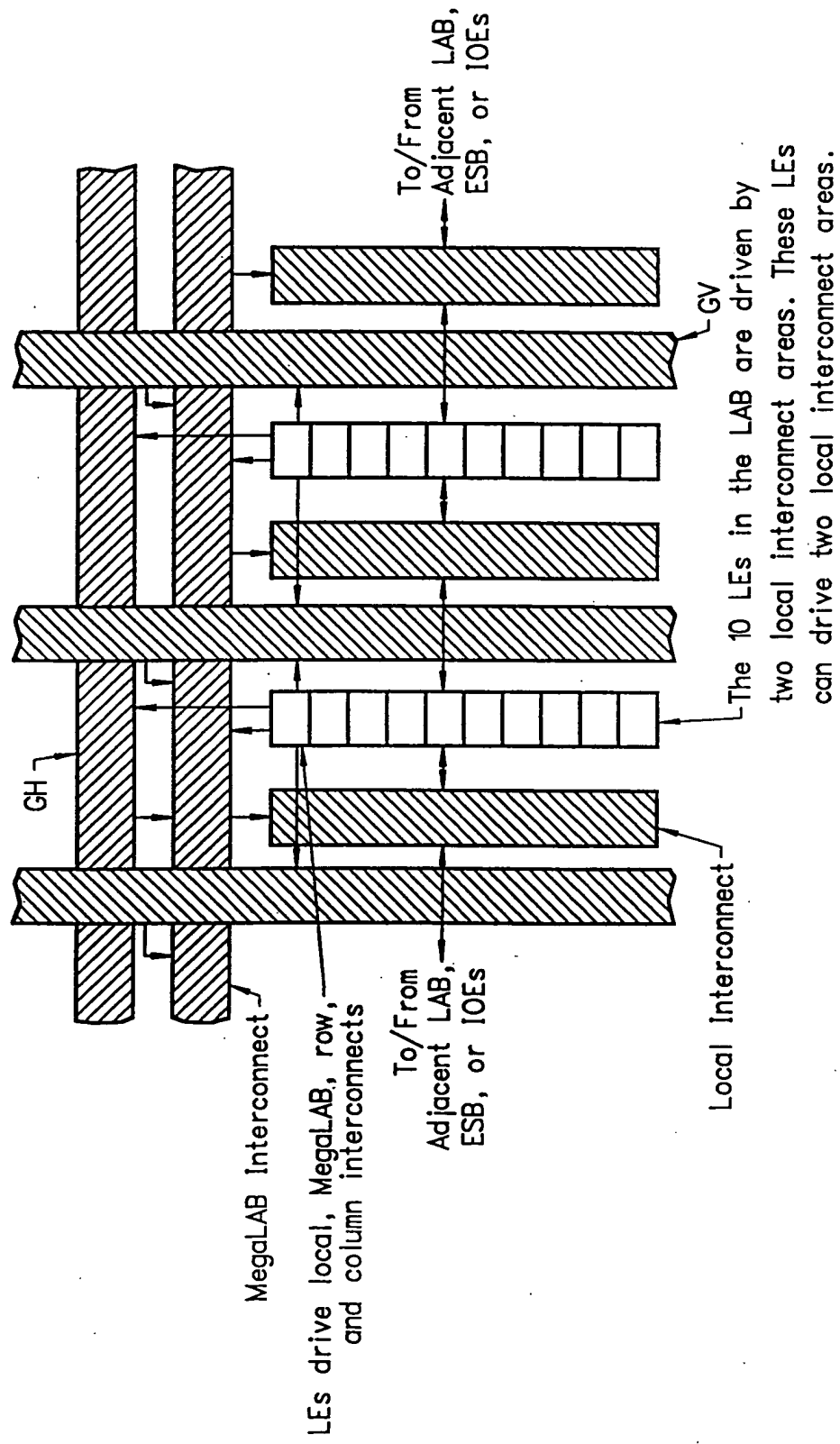


FIGURE 6

09881225-051201

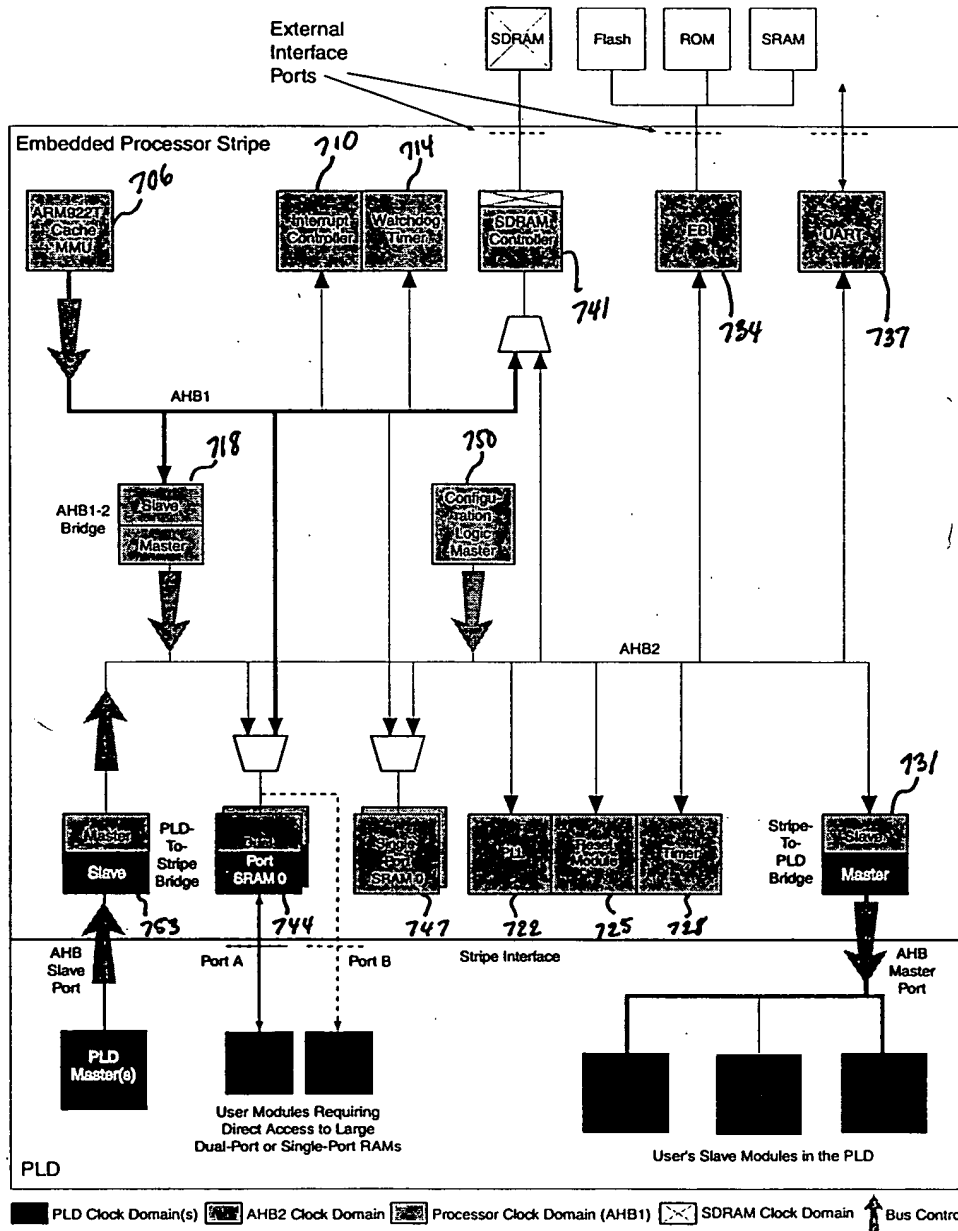


FIGURE 7

FIGURE 8

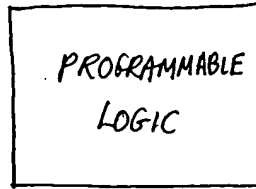


FIGURE 9

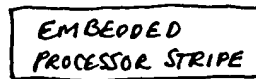


FIGURE 10

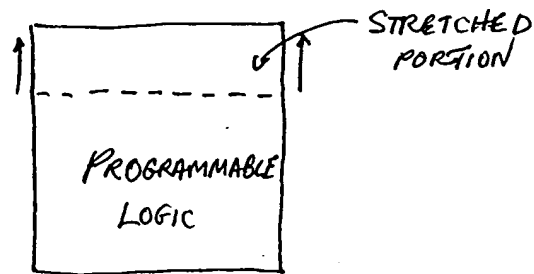
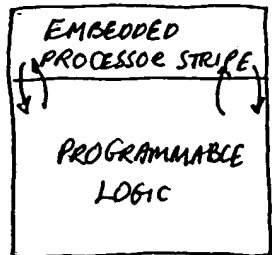


FIGURE 11



09881226-051201

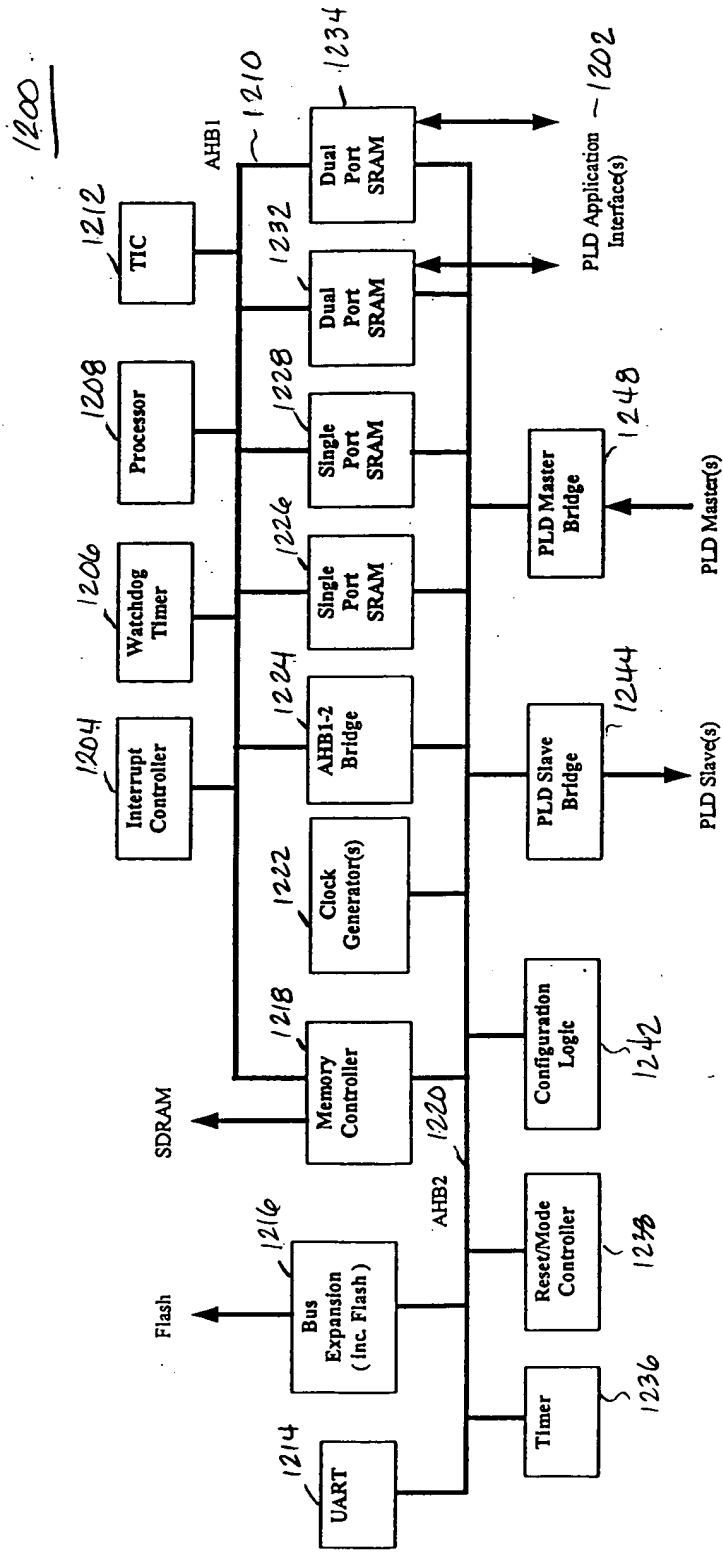


FIGURE 12

32

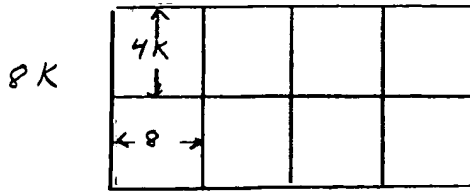


FIGURE 13A

8Kx32

16

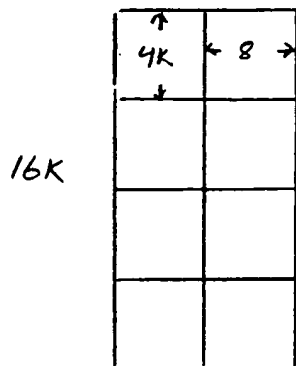


FIGURE 13B

16Kx16

8

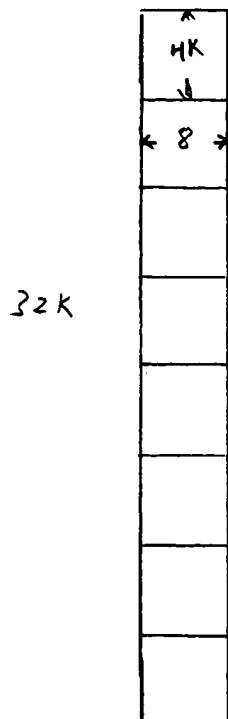


FIGURE 13C

32Kx8

09881226 " 061201

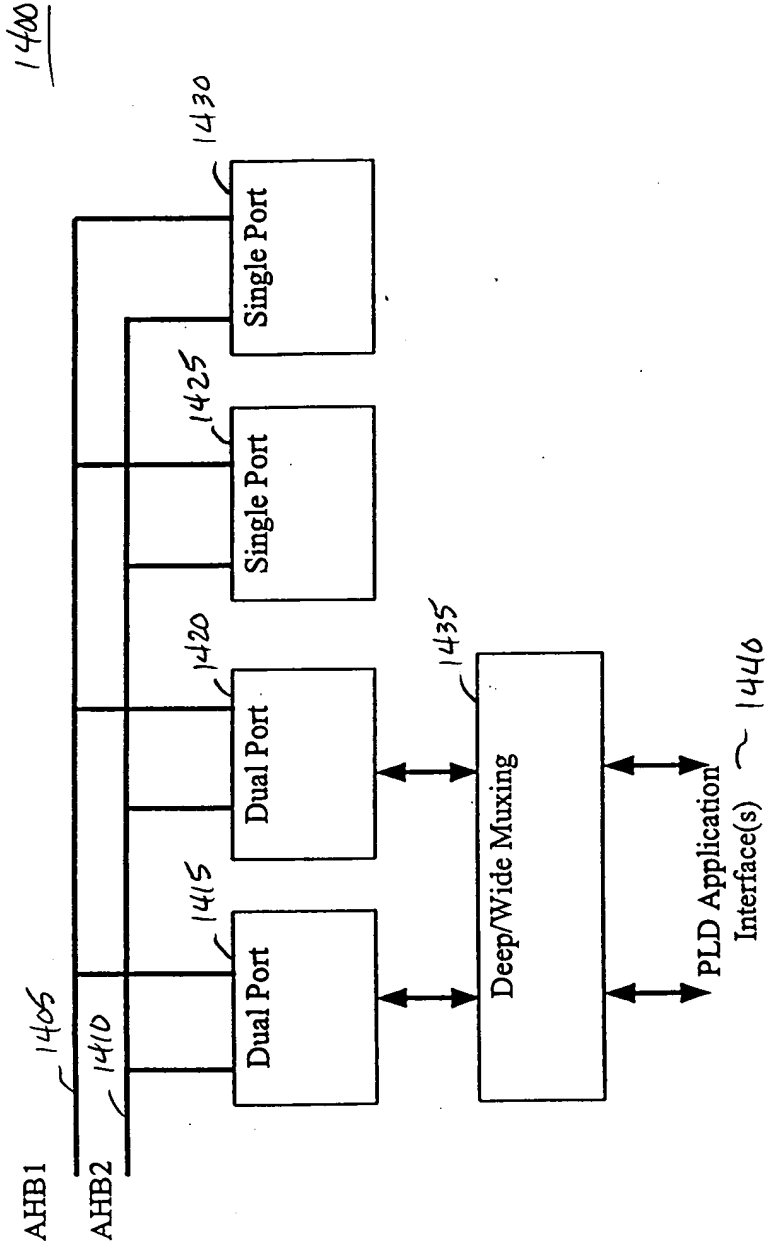


FIGURE 14

1500

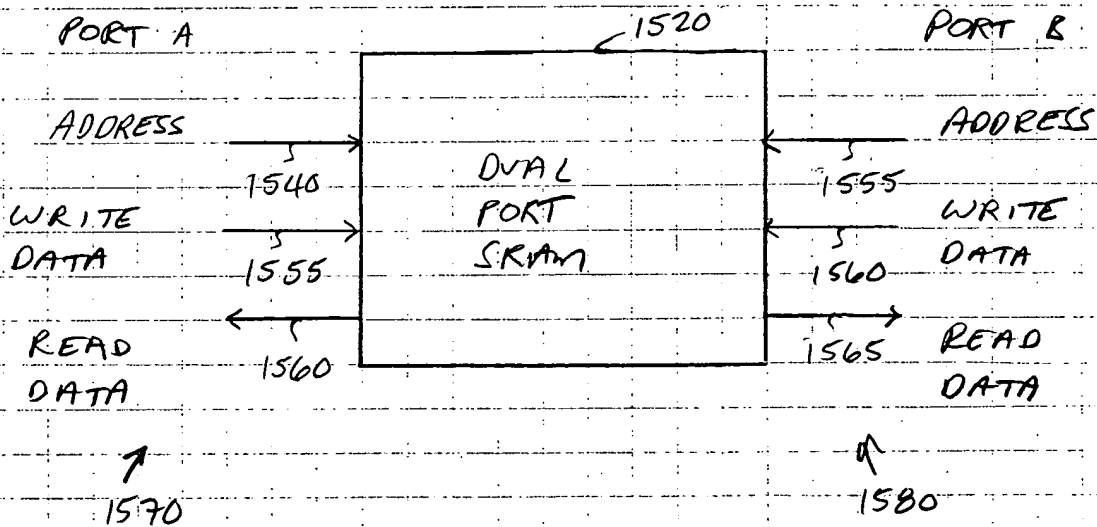
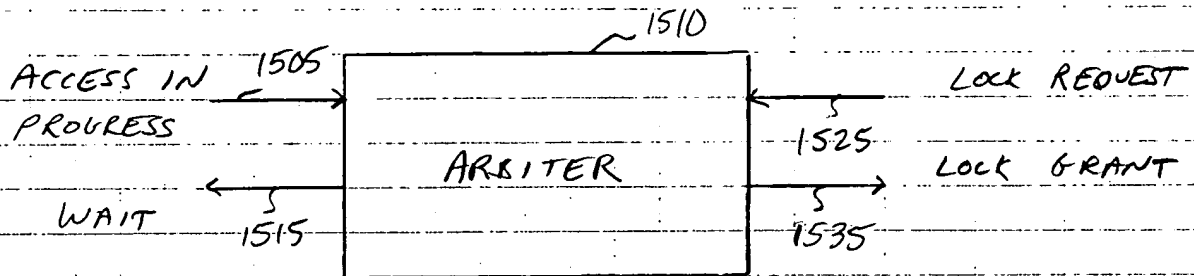
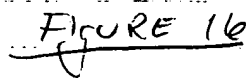


Figure 15

1670
PORT A

PORT B



04347Eh 00101

1700

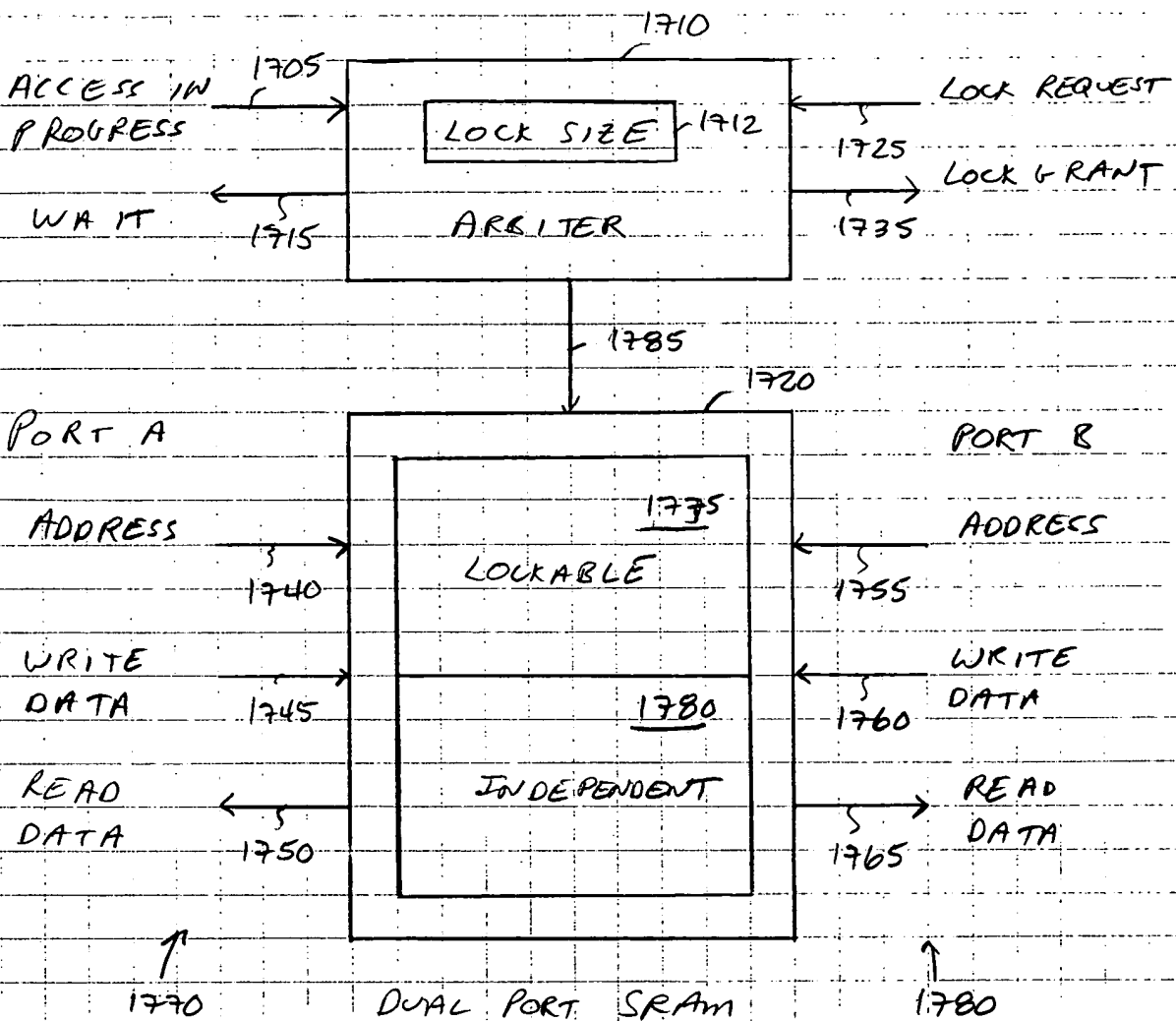


Figure 17

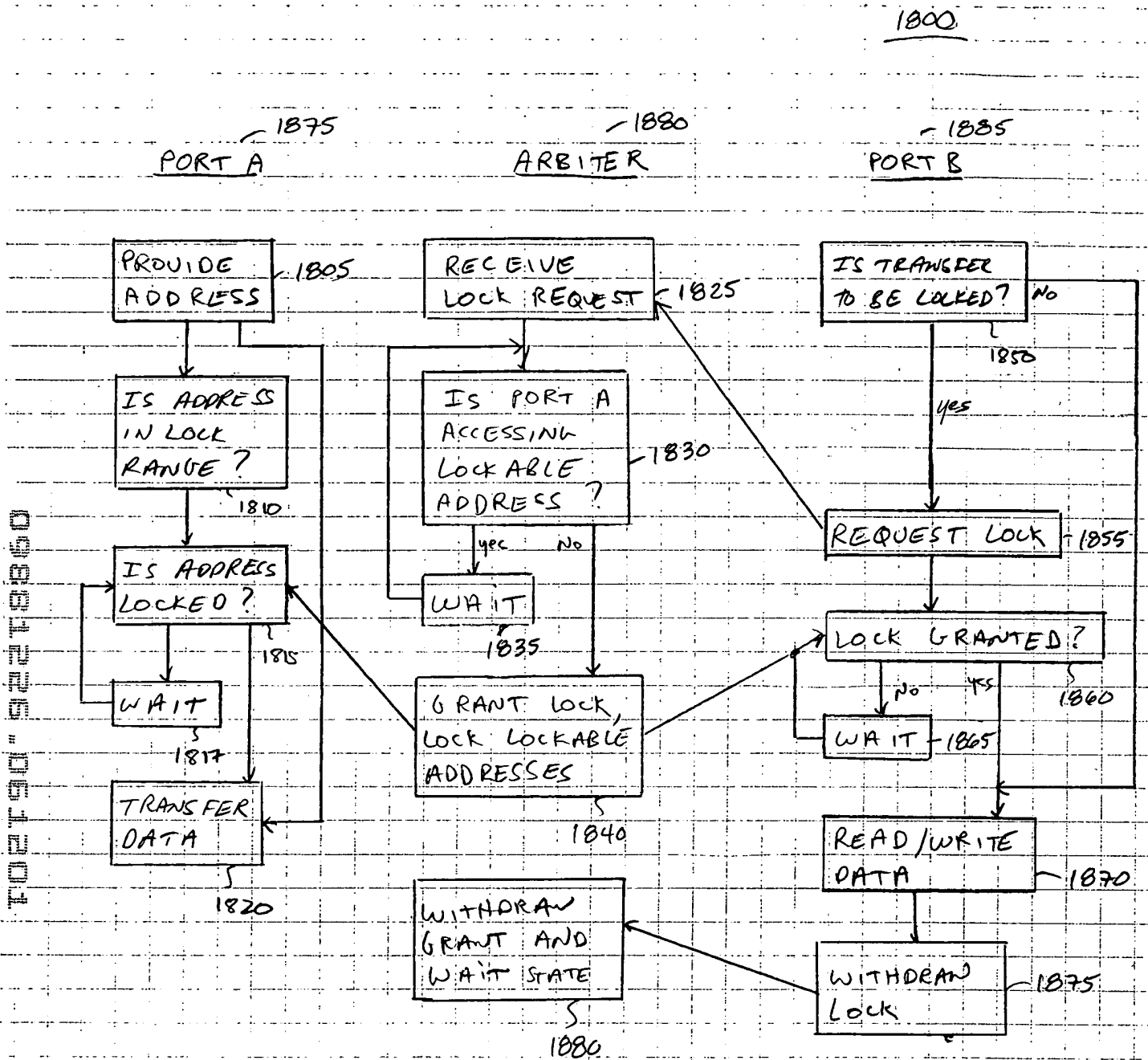


FIGURE 18

1900

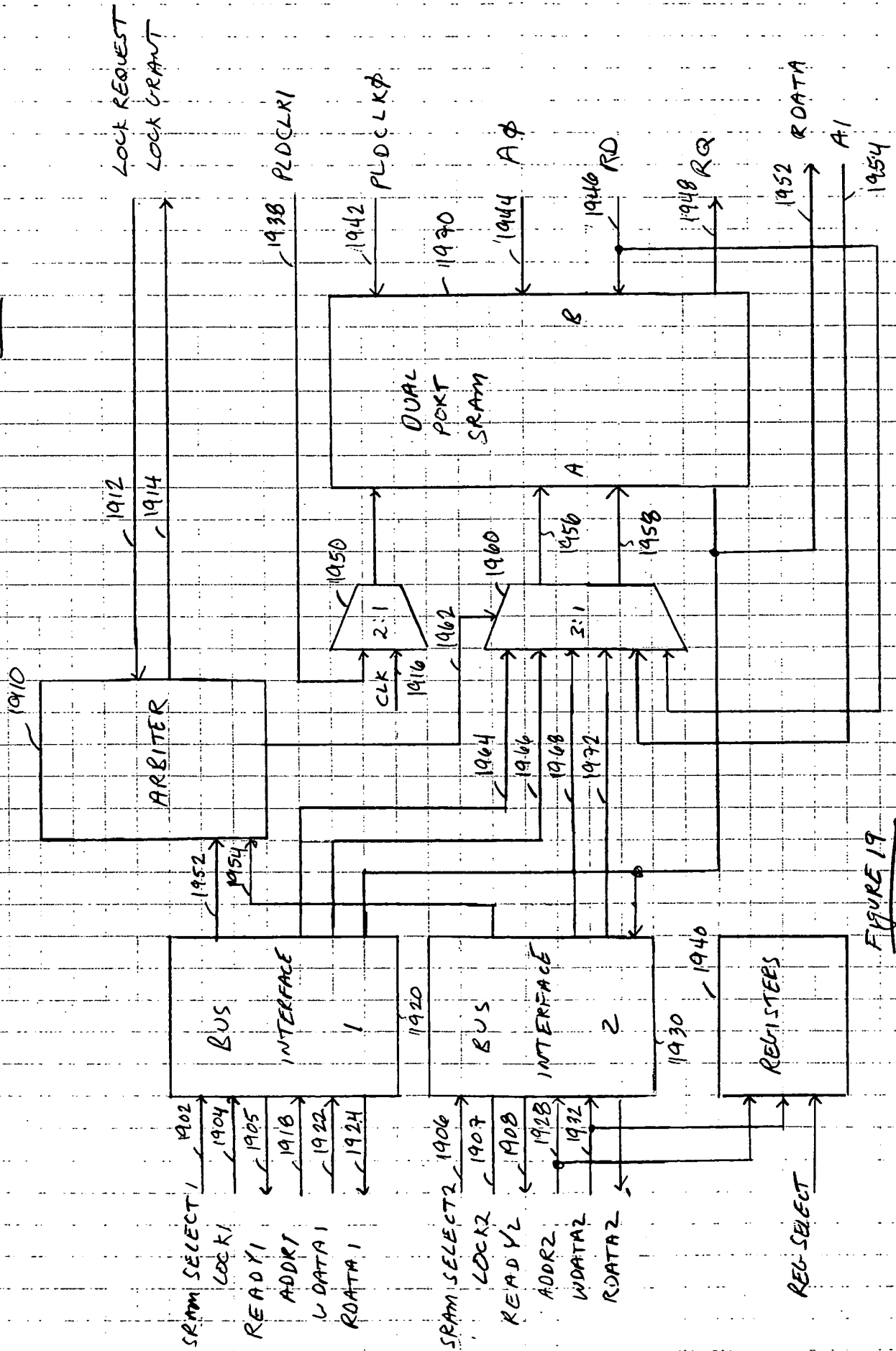


FIGURE 19

2000

PORT A
-2010
INTERFACE 1

ARBITRATION
-2020
BETWEEN
INTERFACE 2

ARBITRATION
-2035
BETWEEN
PORTS A & B

PORT B
-2040

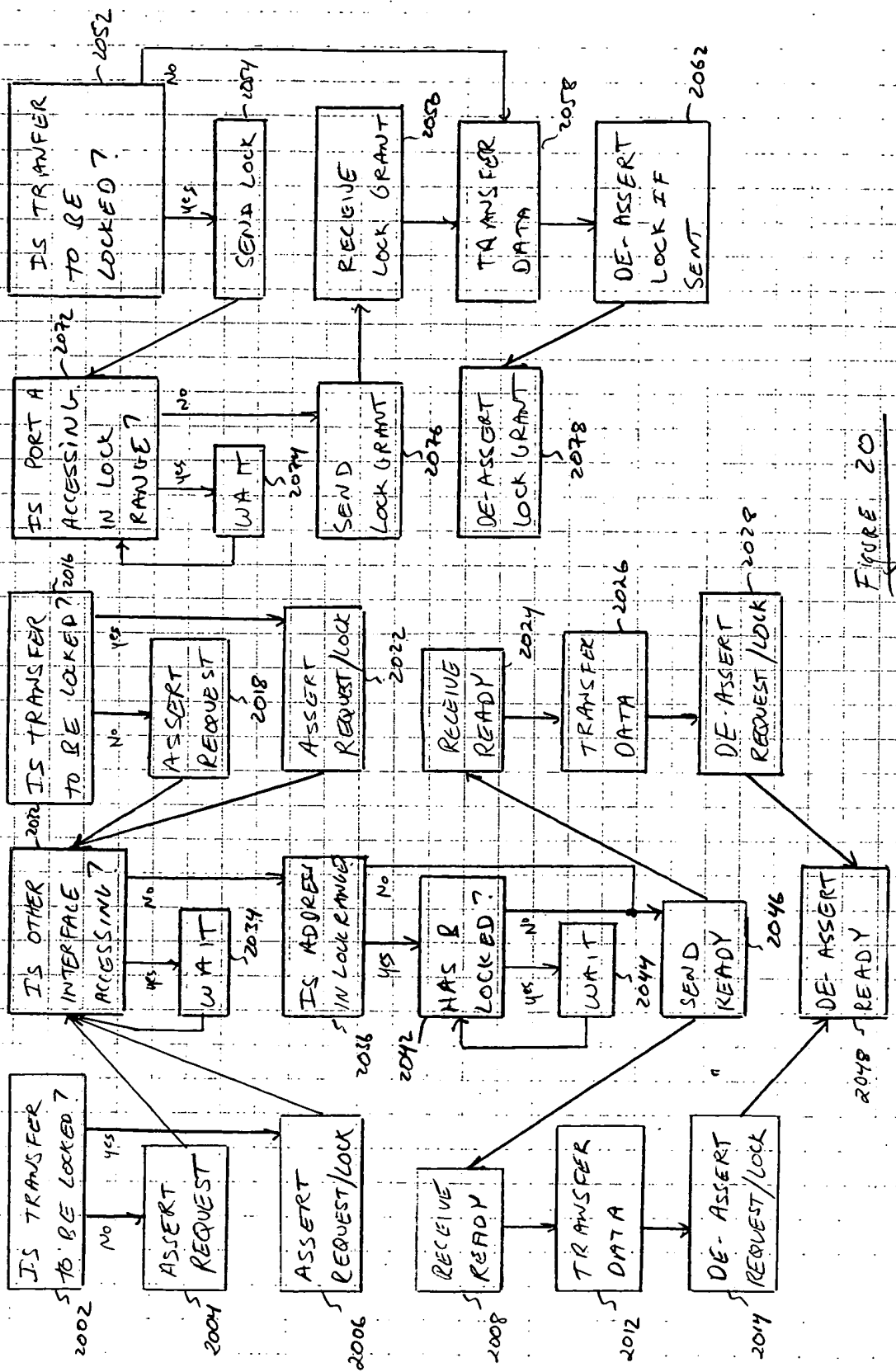


FIGURE 20

2100

2140
INTERFACE 2

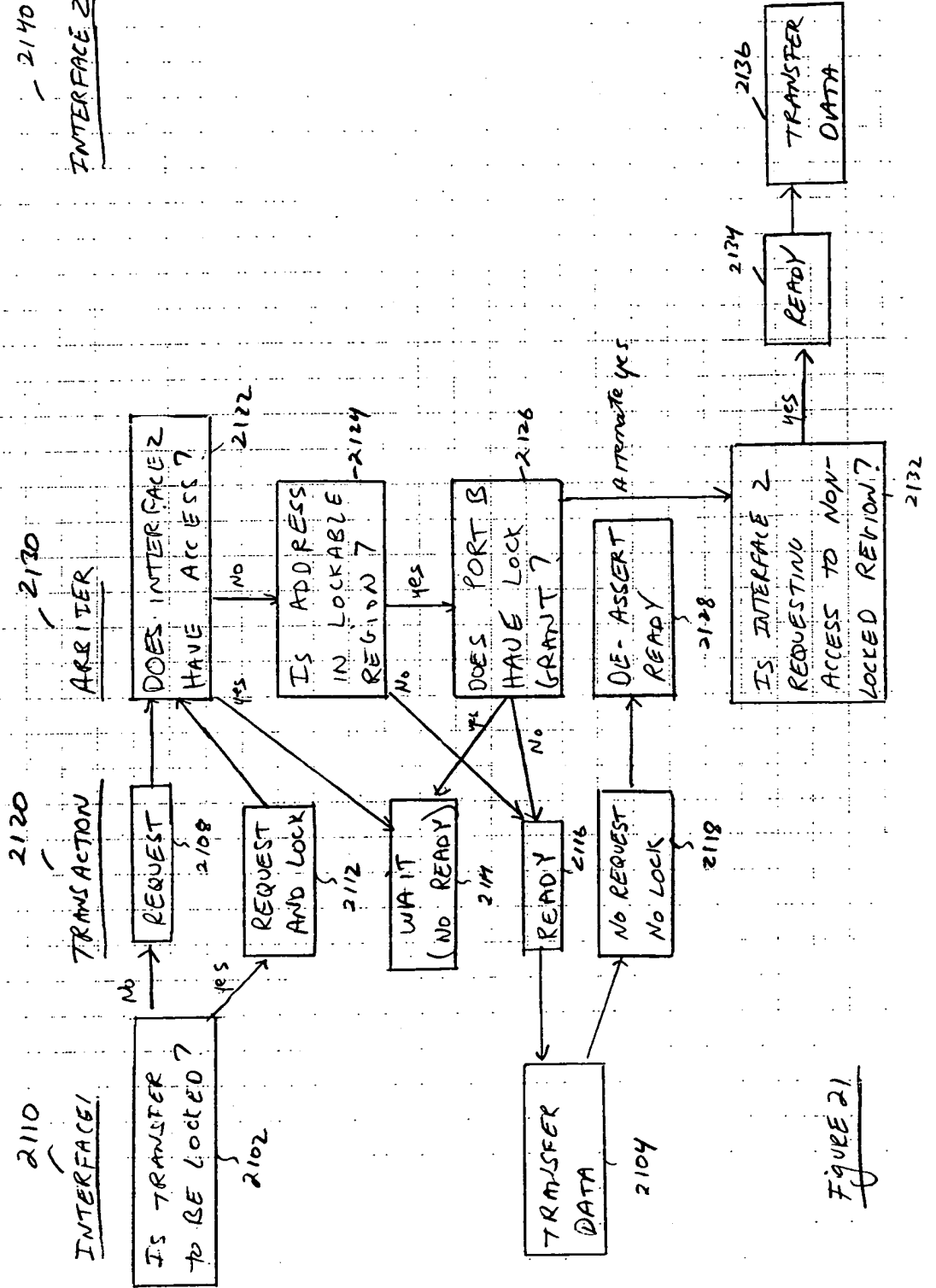
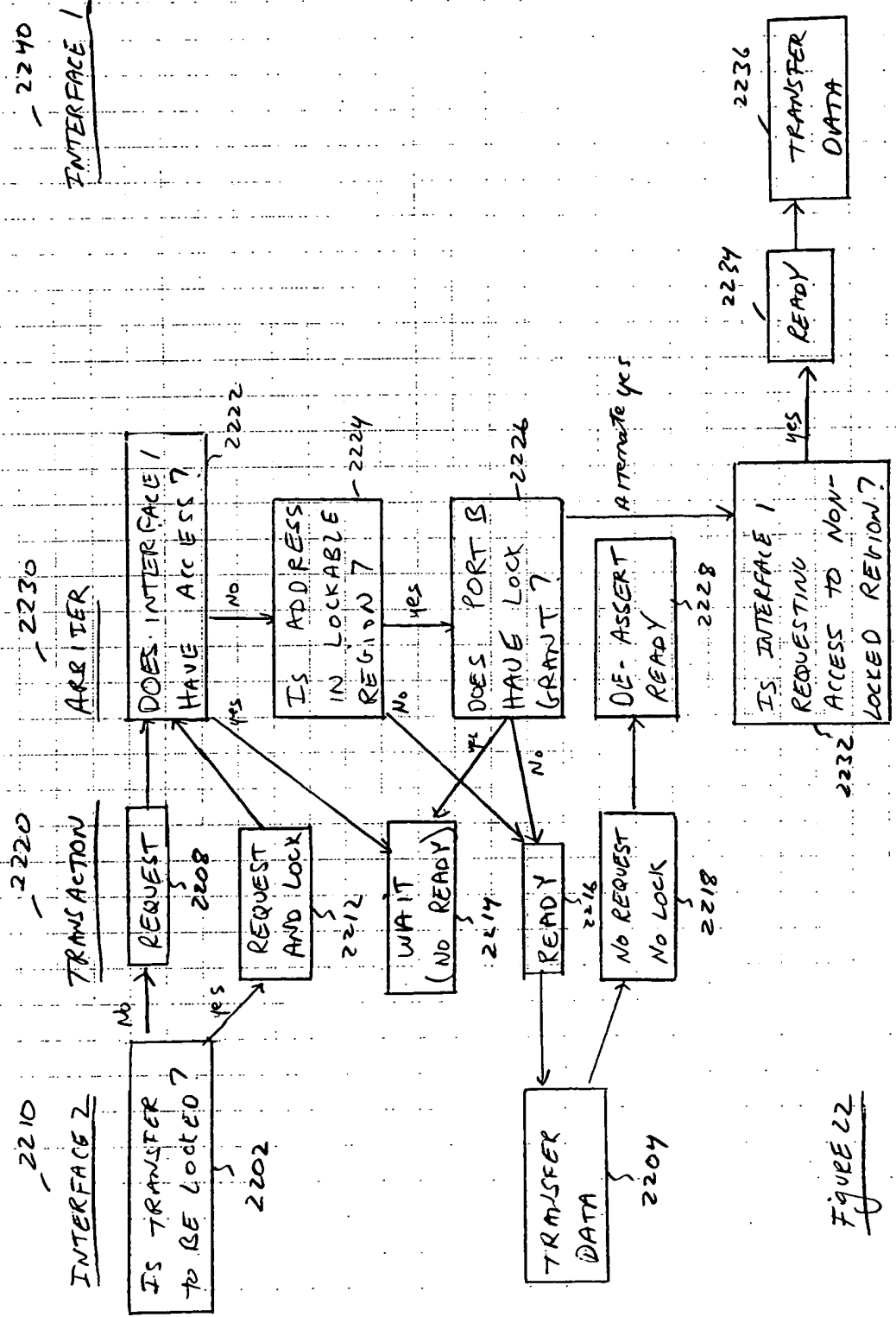


Figure 21



2300

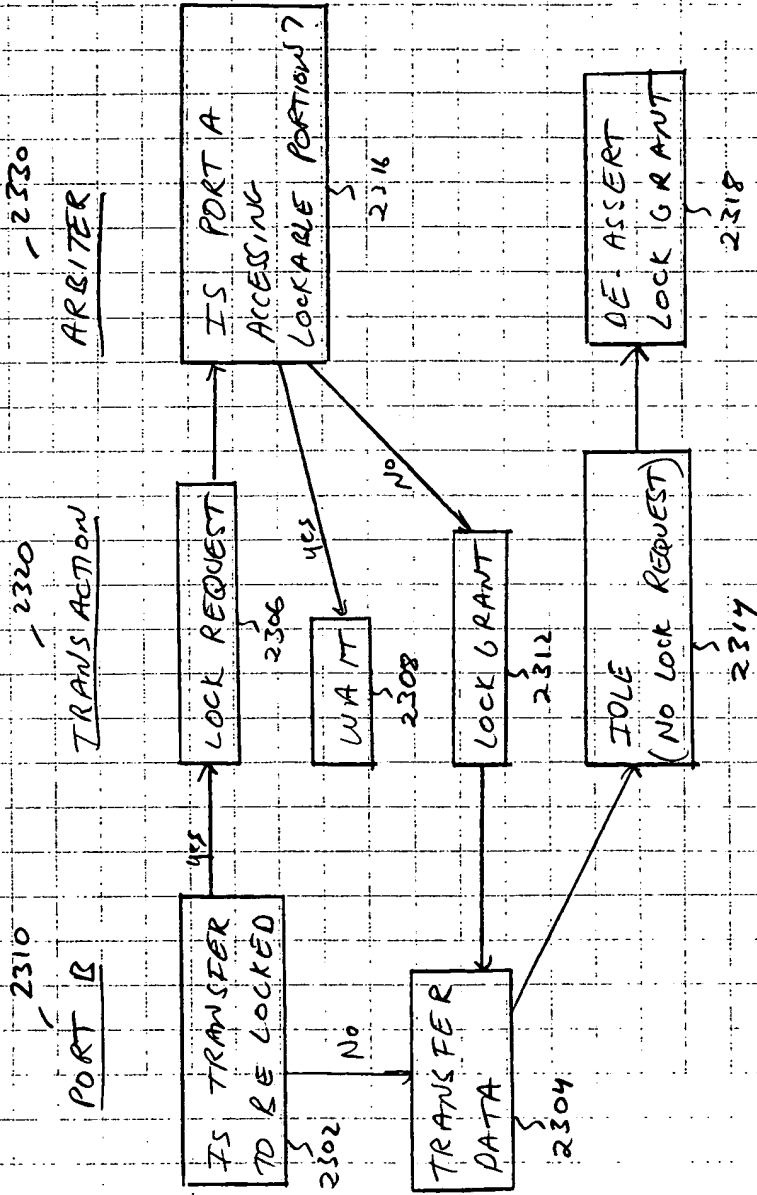


Figure 23

Dual Port SRAM AND ASSOCIATED CIRCUITRY

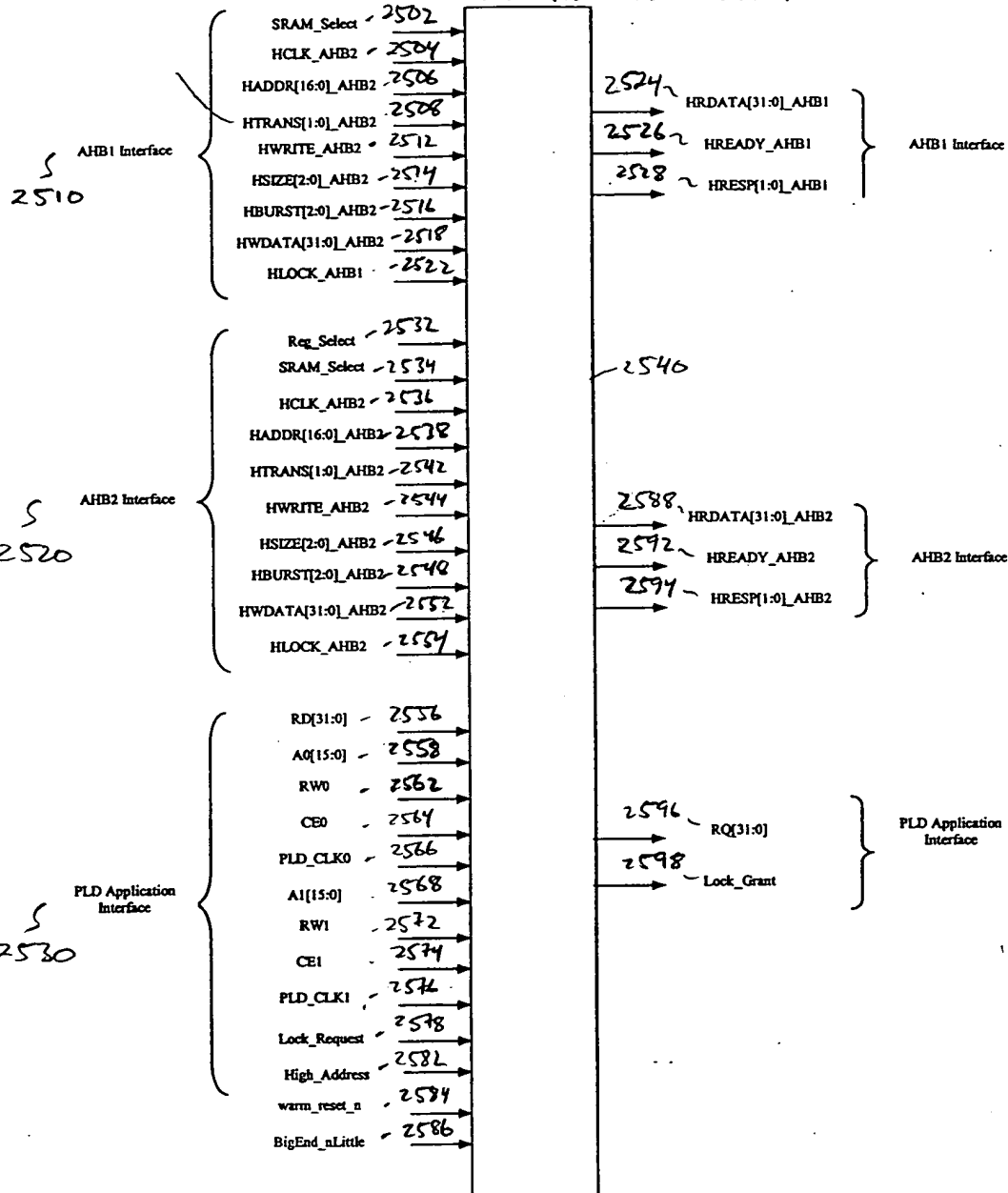


FIGURE 25